

The diagram illustrates a computer system architecture. On the left is a large box labeled "Processor 17". Inside this box is a smaller box labeled "Pipeline 40". To the right of the processor is a vertical line representing the "Interconnect 15". Above this line, an arrow labeled "10" points towards the interconnect. Below the interconnect line are four boxes representing peripheral devices: "Storage device 18", "Keyboard 20", "Display 25", and "Network 30". The storage device box contains two sub-boxes: "Data 35" and "Program instructions 36". Bidirectional arrows connect the interconnect line to each of the four peripheral device boxes.

Fig. 1

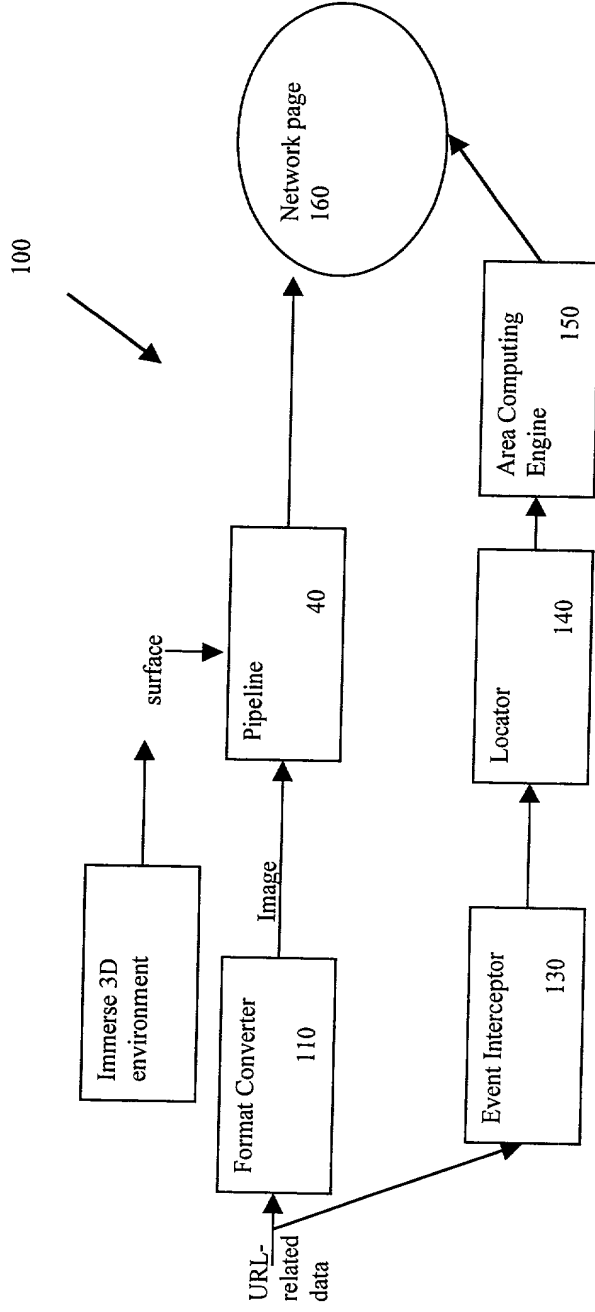


Fig. 2

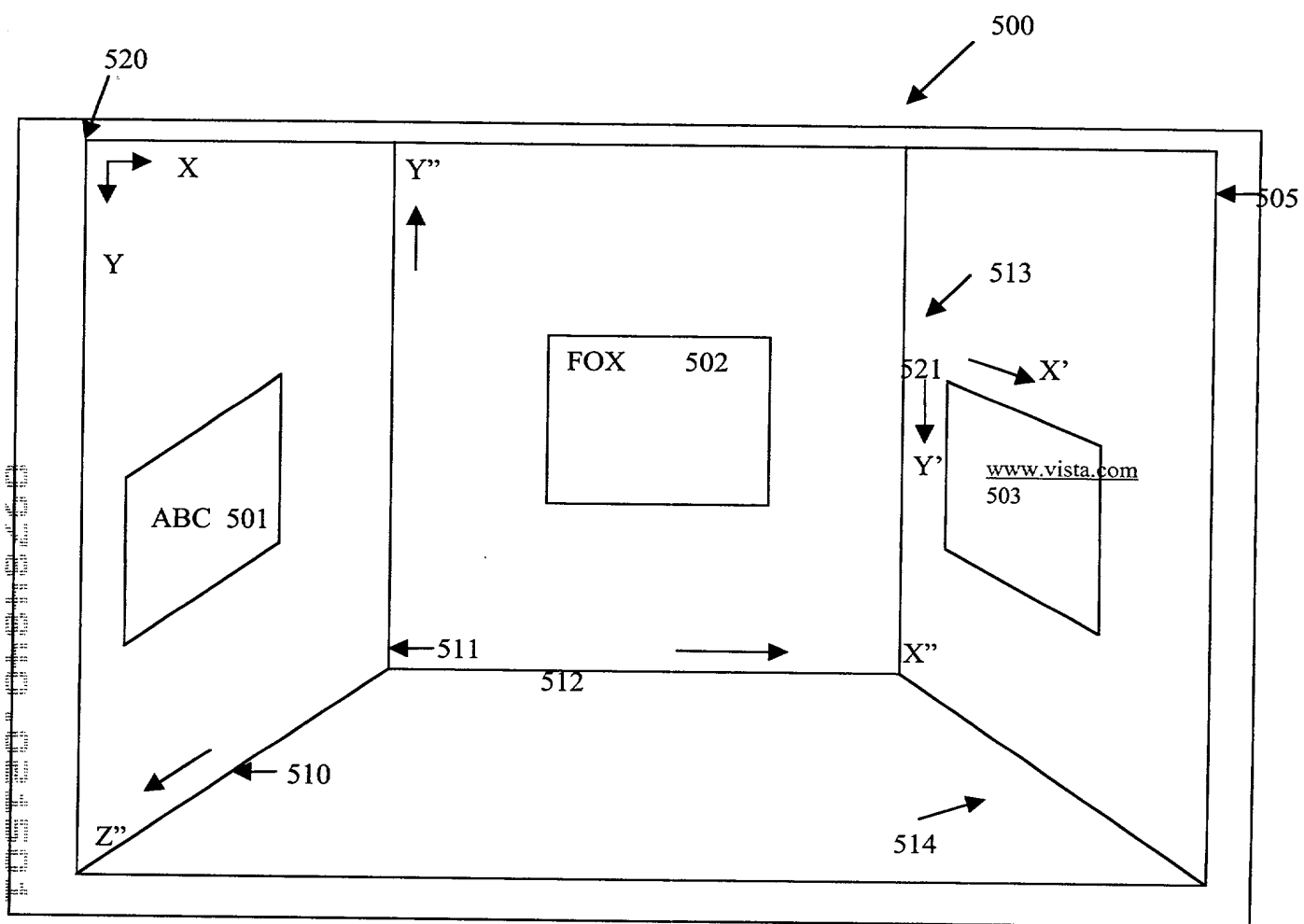


Fig. 3

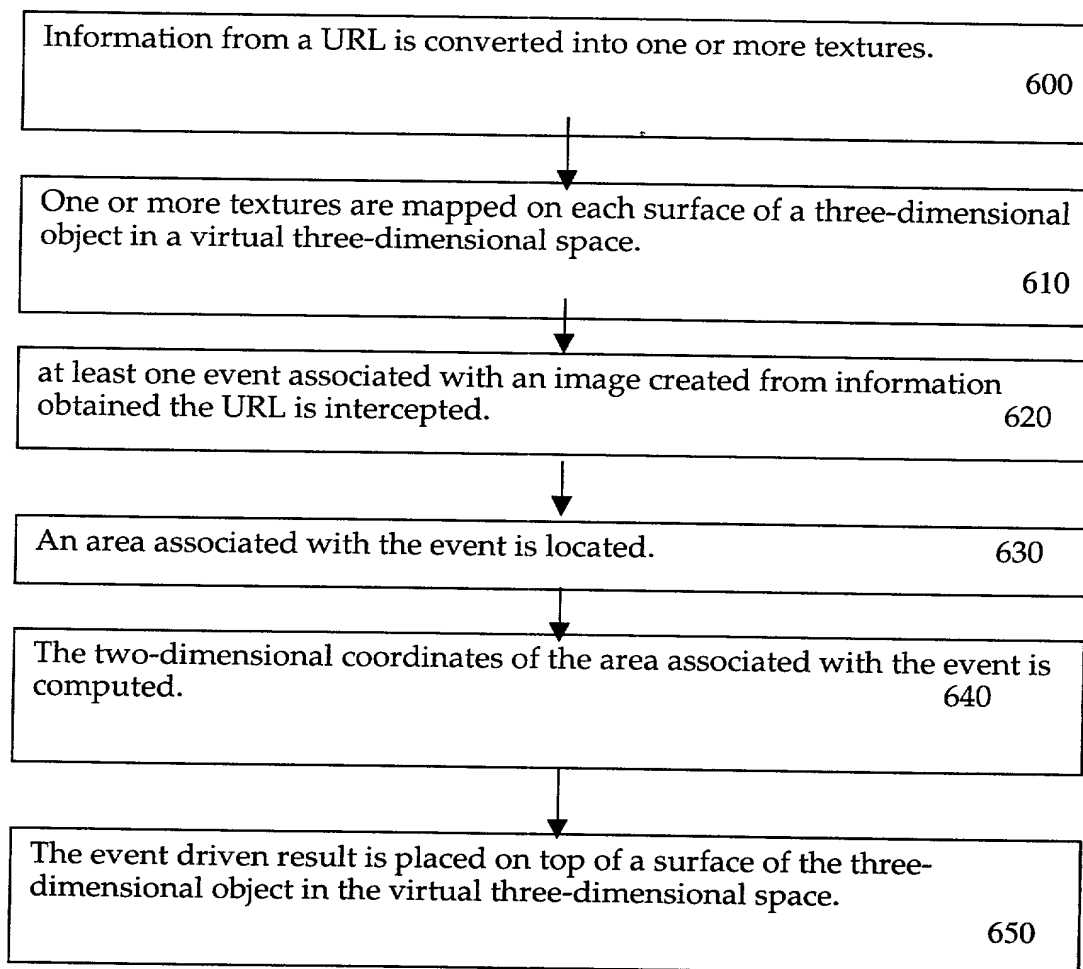


Fig. 4